	Application No.	Applicant(s)
Notice of Allowability	10/762,540	TAKEUCHI, YOSHIAKI
	Examiner	Art Unit
	Thong Q. Le	2818
The MAILING DATE of this communication appears on the cover sheet with the correspondence address All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.		
1. This communication is responsive to		
2. The allowed claim(s) is/are <u>1-16</u> .		
3. The drawings filed on 23 January 2004 are accepted by the Examiner.		
<ul> <li>4.  Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).</li> <li>a)  All b)  Some* c)  None of the:</li> <li>1.  Certified copies of the priority documents have been received.</li> <li>2.  Certified copies of the priority documents have been received in Application No</li> <li>3.  Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).</li> <li>* Certified copies not received:</li> </ul>		
Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.  THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.		
5. A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.		
<ul> <li>6. CORRECTED DRAWINGS (as "replacement sheets") must be submitted.</li> <li>(a) including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached</li> <li>1) hereto or 2) to Paper No./Mail Date</li> <li>(b) including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date</li> <li>Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).</li> <li>7. DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the</li> </ul>		
attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.		
<ul> <li>Attachment(s)</li> <li>1. ☑ Notice of References Cited (PTO-892)</li> <li>2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948)</li> <li>3. ☑ Information Disclosure Statements (PTO-1449 or PTO/SB/0 Paper No./Mail Date</li></ul>	6. ☐ Interview Summary Paper No./Mail Dat 8), 7. ☐ Examiner's Amendr	te

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## **DETAILED ACTION**

1. Claims 1-16 are presented for examination.

## Information Disclosure Statement

- 2. This office acknowledges receipt of the following items from the Applicant:
  Information Disclosure Statement (IDS) filed on 01/03/2004.
- 3. Information disclosed and list on PTO 1449 was considered.

## **Priority**

4. Receipt is acknowledged of papers submitted under 35 U.S.C. 119(a)-(d), which papers have been placed of record in the file.

## Reasons for Allowance

5. Claims 1-16 are allowed.

The following is an examiner's statement of reasons for allowance:

Claims 1-16 include allowable subject matter since the prior art made of record and considered pertinent to the applicant's disclosure does not teach or suggest the claimed limitations. Coker et al. (U.S. Patent No. 5,258,952), and others, does not teach the claimed invention having a semiconductor integrated circuit device comprises a mode determination circuit which determines a start of a mode in which column access is executed and generates a mode determination signal when a condition that allows a start of a column access operation of the memory cell array is satisfied, and the second address transition detection circuit detects the transition the column address, or determines an end of column access and sets a standby state when column access starts, and transition of row address is detected, wherein when the mode determination

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memory cell array.

circuit determines row access, the access operation of the predetermined address or memory cell array controlled by the timeout circuit in the first control circuit in read and write operations for the memory cell array, and when the mode determination circuit determines column access, an active operation is continued while stopping control by the timeout circuit until column access is ended in the read and write operations for the

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Any inquiry concerning this communication or earlier communications from the examiner should be directed to Thong Q. Le whose telephone number is 571-272-1783. The examiner can normally be reached on 8:00am-5:00pm M-F.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, David C. Nelms can be reached on 571-272-1787. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Thong Q. Le
Primary Examiner
Art Unit 2818

THONG LE